

3.1.5 Design and Analysis of Integrated Circuits

Module title: Design and Analysis of Integrated Circuits

Module summary
Module code: EITM 220I
Module coordinator: Prof. Dr. Herman Jallli Ng
Credits (ECTS): 5 CP
workload: in lecture 60 h, independent study time 90 h
Semester: 1 st or 2 nd semester
Pre-requisites with regard to content: Electronics, high-frequency techniques
Pre-requisites according to the examination regulations: none
<p>Competencies: Upon successful completion the students,</p> <ul style="list-style-type: none"> • learn about modern semiconductor technologies that enable the implementation of highly integrated circuits • become highly proficient in advanced circuit techniques and high-frequency basics • are able to design and analyze various integrated circuit blocks using transistors and other semiconductor devices • know how to calculate all design parameters of the circuits • are able to optimize the performance of circuit blocks regarding gain, noise, stability, dynamic range, efficiency and total power consumption
<p>Assessment:</p> <p>Assessment is done by either a written exam (120 minutes) or an oral examination (20 minutes). The form of examination will be announced at the beginning of the semester</p>
<p>Usability:</p> <p><i>General:</i> The module imparts knowledge of semiconductor technologies, microelectronics circuits, advanced transistor-level design techniques, integrated circuit building blocks and transceiver architectures. Critical design parameters of the integrated circuit building blocks are discussed and the optimization methods are introduced. Examples of highly integrated transceivers, high-frequency systems, various fully-integrated building blocks on transistor-level are presented in this module.</p> <p><i>Connection with other modules:</i> Design and Analysis of Integrated Circuits require a comprehensive background in fundamental of electrical engineering as well as profound knowledge in electronic and semiconductor components as well as basic transistor circuits. Proficiency in high-frequency techniques are also required.</p>

Course: Design and Analysis of Analog ICs
Module code: EITM 221I
Lecturer: Prof. Dr. Herman Jalli Ng
Contact hours: by arrangement
Semester of delivery: yearly, summer semester
Type/mode: lecture 2h/week; mandatory in the study field Information Technology, optional in the other study fields of the program
Language of instruction: English or German; the course language will be announced at the beginning of the semester

Content: <ul style="list-style-type: none"> • Advanced Circuit Techniques • Review of Amplifiers • Frequency Response of Amplifiers • Noise • Feedback • Operational amplifiers • Oscillators • Phase-Locked Loops
Recommended reading: Razavi B.: Design of Analog CMOS Integrated Circuits, McGraw Hill Education, 2015 Baker R.J.: CMOS Circuit Design, Layout and Simulation, Wiley-IEEE, 2010
Comments: -

Course: Design and Analysis of RF ICs
Module code: EITM 222I
Lecturer: Prof. Dr. Herman Jalli Ng
Contact hours: by arrangement
Semester of delivery: yearly, summer semester
Type/mode: lecture 2h/week; mandatory in the study field Information technology, optional in the other study fields of the program
Language of instruction: English or German; the course language will be announced at the beginning of the semester
Content: <ul style="list-style-type: none"> • Transceiver Architectures • High-Frequency Devices • S-Parameters and Impedance Matching • Low-Noise Amplifiers • Power Amplifiers • Mixers • Voltage-Controlled Oscillator
Recommended reading: Voinigescu S.: High-Frequency Integrated Circuits, The Cambridge RF and Microwave Engineering Series, 1st edition, 2013 Razavi B.: RF Microelectronics, Prentice Hall, 2011 Ellinger F.: Radio Frequency Integrated Circuits and Technologies, Springer, 2007
Comments: -